Product data sheet

1. General description

NPN low V_{CEsat} transistor in a leadless ultra small DFN1010D-3 (SOT1215) Surface-Mounted Device (SMD) plastic package with visible and solderable side pads.

PNP complement: PBSS5260QA-Q

2. Features and benefits

- Very Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- High collector current gain h_{FE} at high I_C
- · High energy efficiency due to less heat generation
- Reduced Printed-Circuit Board (PCB) area requirments
- Solderable side pads
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Loadswitch
- Battery-driven devices
- Power management
- Charging circuits
- · Power switches (e.g. motors, fans)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	60	V
I _C	collector current		-	-	2	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	3	Α
R _{CEsat}	collector-emitter saturation resistance	I_C = 1 A; I_B = 0.1 A; pulsed; $t_p \le 300 \ \mu s$; δ ≤ 0.02; T_{amb} = 25 °C	-	130	180	mΩ



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	3	
2	Е	emitter		C
3	С	collector	4	В
4	С	collector	1 2	E
			Transparent top view DFN1010D-3 (SOT1215)	sym123

6. Ordering information

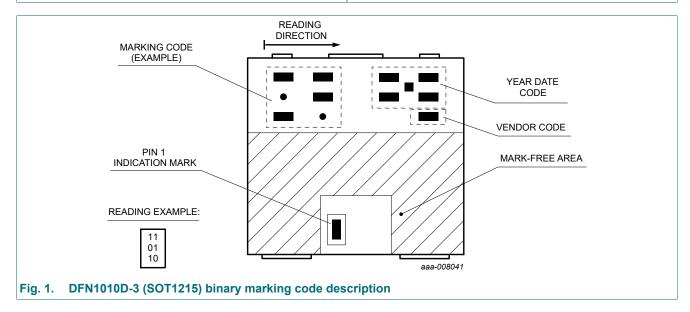
Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PBSS4260QA-Q		plastic, leadless thermal enhanced ultra thin small outline package with side-wettable flanks (SWF); 3 terminals; 0.75 mm pitch; 1.1 mm x 1 mm x 0.37 mm body	<u>SOT1215</u>			

7. Marking

Table 4. Marking codes

Type number	Marking code
PBSS4260QA-Q	11
	11
	00



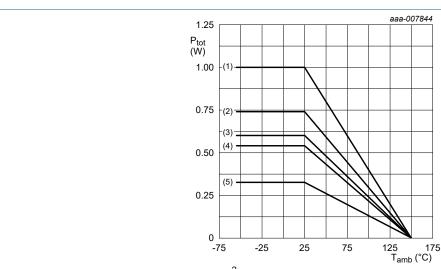
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	60	V
V _{CEO}	collector-emitter voltage	open base		-	60	V
V _{EBO}	emitter-base voltage	open collector		-	7	V
I _C	collector current			-	2	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	3	Α
I _B	base current			-	0.3	Α
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms		-	1	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	325	mW
			[2]	-	600	mW
			[3]	-	740	mW
			[4]	-	540	mW
			[5]	-	1000	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on an FR4 PCB single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm².
- [4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.
- [5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated mounting pad for collector 1 cm².



- (1) FR4 PCB, 4-layer copper, 1 cm²
- (2) FR4 PCB, single-sided copper, 6 cm²
- (3) FR4 PCB, single-sided copper, 1 cm²
- (4) FR4 PCB, 4-layer copper, standard footprint
- (5) FR4 PCB, single-sided copper, standard footprint

Fig. 2. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j-a)	thermal resistance from		[1]	-	-	385	K/W
	junction to ambient		[2]	-	-	209	K/W
			[3]	-	-	169	K/W
			[4]	-	-	232	K/W
			[5]	-	-	125	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm².
- [4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.
- [5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated mounting pad for collector 1 cm².

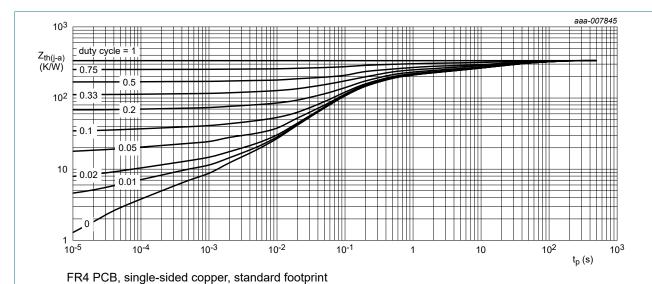


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

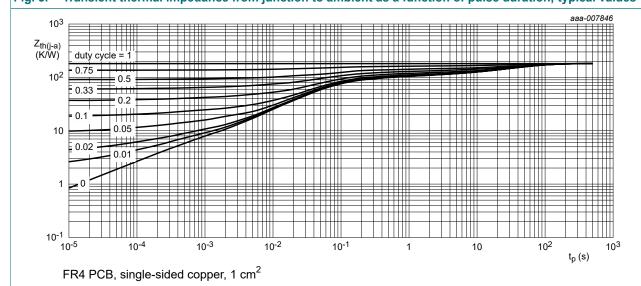


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

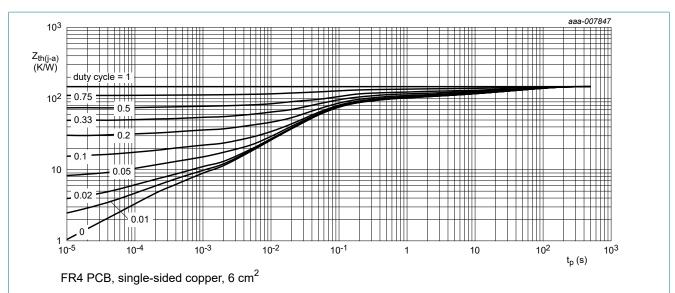
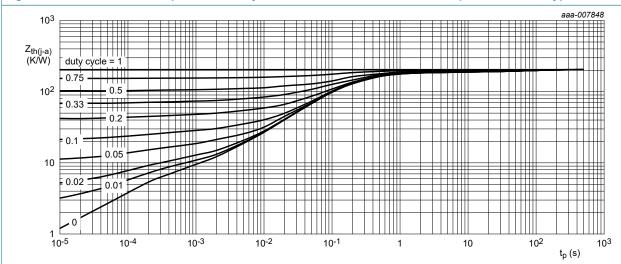


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, 4-layer copper, standard footprint

Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

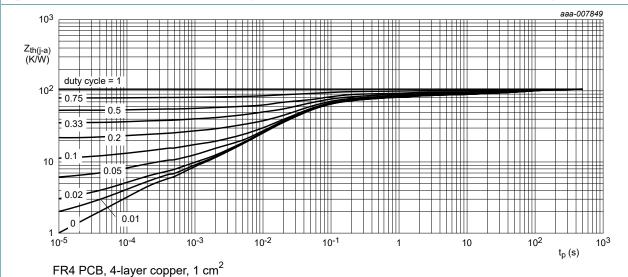


Fig. 7. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
І _{сво}	collector-base cut-off	V _{CB} = 48 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current	V _{CB} = 48 V; I _E = 0 A; T _j = 150 °C	-	-	50	μΑ
I _{CES}	collector-emitter cut-off current	V _{CE} = 48 V; V _{BE} = 0 V; T _{amb} = 25 °C	-	-	100	nA
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V_{CE} = 2 V; I_{C} = 100 mA; pulsed; $t_{p} \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	235	400	-	
		V_{CE} = 2 V; I_{C} = 500 mA; pulsed; $t_{p} \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	150	240	-	
		V_{CE} = 2 V; I_{C} = 1 A; pulsed; $t_{p} \le 300 \ \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C	85	125	-	
		V_{CE} = 2 V; I_{C} = 2 A; pulsed; $t_{p} \le 300 \ \mu s$; δ ≤ 0.02; T_{amb} = 25 °C	40	65	-	
V _{CEsat}	collector-emitter saturation voltage	I_C = 500 mA; I_B = 50 mA; pulsed; $t_p \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	-	75	100	mV
		I_C = 1 A; I_B = 50 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	145	190	mV
		I_C = 1 A; I_B = 100 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	130	180	mV
		I_C = 2 A; I_B = 100 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	275	370	mV
		I_C = 2 A; I_B = 200 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	250	350	mV
R _{CEsat}	collector-emitter saturation resistance	I_C = 1 A; I_B = 0.1 A; pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$; T_{amb} = 25 °C	-	130	180	mΩ
V_{BEsat}	base-emitter saturation voltage	I_C = 500 mA; I_B = 50 mA; pulsed; $t_p \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	-	0.88	1	V
		I_C = 1 A; I_B = 50 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	0.91	1.05	V
		I_C = 2 A; I_B = 100 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	1	1.15	V
		I_C = 2 A; I_B = 200 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	1.05	1.2	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = 2 V; I_{C} = 0.5 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	0.77	0.9	V
t _d	delay time	$V_{CC} = 10 \text{ V}; I_C = 0.5 \text{ A}; I_{Bon} = 25 \text{ mA};$	-	15	-	ns
r	rise time	I _{Boff} = -25 mA; T _{amb} = 25 °C	-	85	-	ns
on	turn-on time		-	100	-	ns
s	storage time		-	545	-	ns
t _f	fall time		-	125	-	ns
t _{off}	turn-off time		-	670	-	ns
f⊤	transition frequency	V _{CE} = 10 V; I _C = 50 mA; f = 100 MHz; T _{amb} = 25 °C	120	180	-	MHz
C _c	collector capacitance	V_{CB} = 10 V; I_{E} = 0 A; i_{e} = 0 A; f = 1 MHz; T_{amb} = 25 °C	-	4.7	6	pF

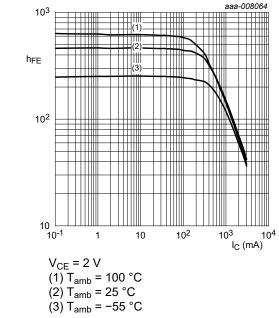


Fig. 8. DC current gain as a function of collector current; typical values

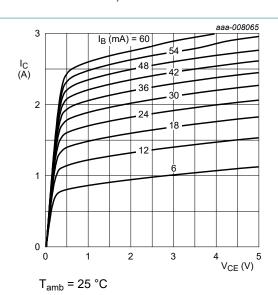
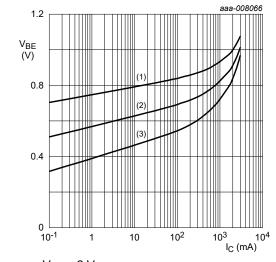


Fig. 9. Collector current as a function of collectoremitter voltage; typical values



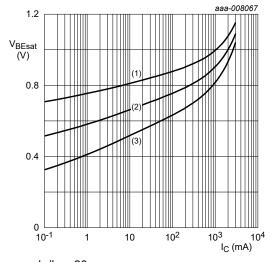
 $V_{CE} = 2 V$

(1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = 100 \, ^{\circ}C$

Fig. 10. Base-emitter voltage as a function of collector current; typical values



 $I_{\rm C}/I_{\rm B}=20$

(1) $T_{amb} = -55$ °C

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = 100 \, ^{\circ}C$

Fig. 11. Base-emitter saturation voltage as a function of collector current; typical values

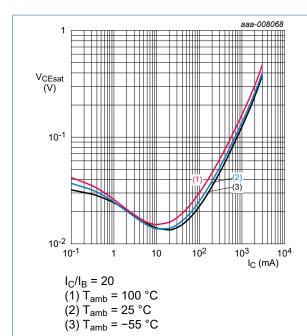


Fig. 12. Collector-emitter saturation voltage as a function of collector current; typical values

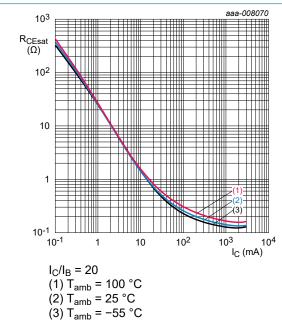


Fig. 14. Collector-emitter saturation resistance as a function of collector current; typical values

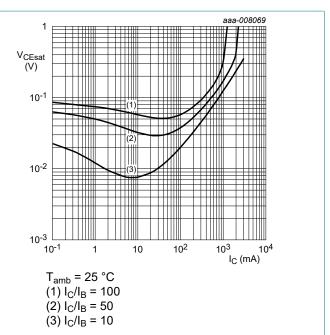


Fig. 13. Collector-emitter saturation voltage as a function of collector current; typical values

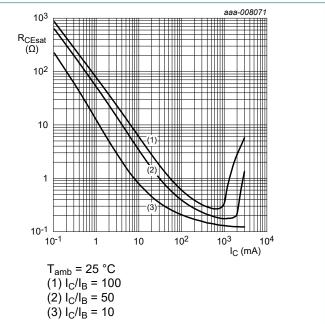
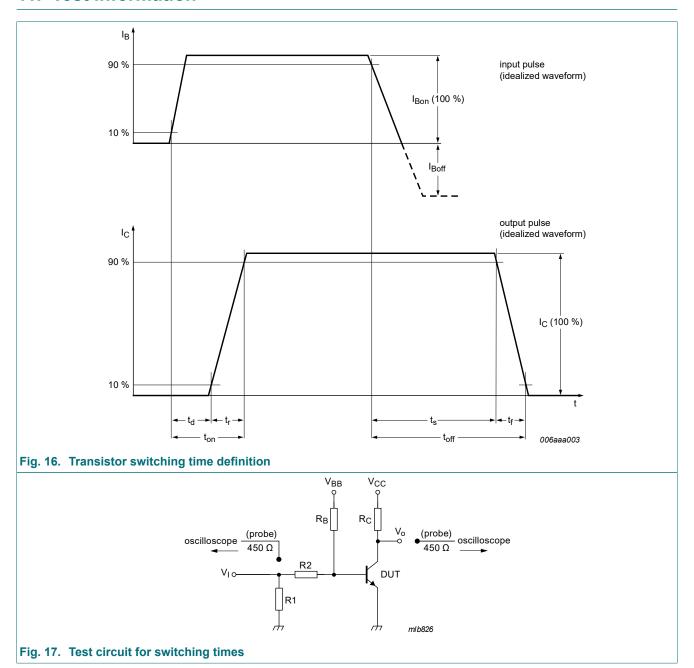


Fig. 15. Collector-emitter saturation resistance as a function of collector current; typical values

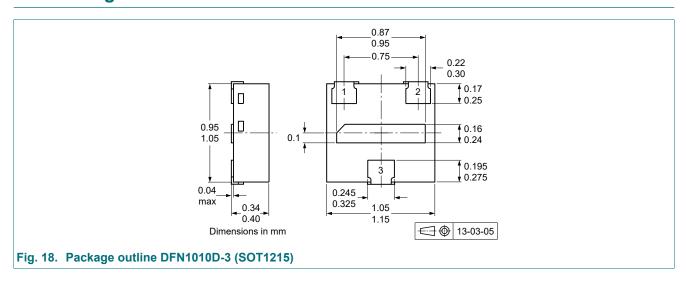
11. Test information



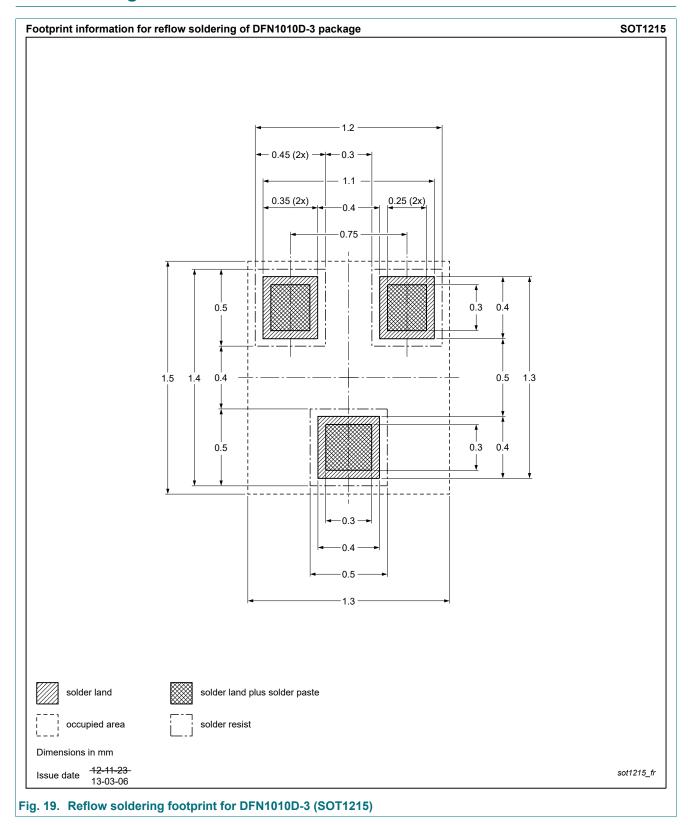
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS4260QA-Q v.1	20250707	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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